

SSCS TAINAN CHAPTER

Soon-Jyh Chang

Nov. 16, 2009




OUTLINE

- Staff List & Website
- Activities and Accomplishments
- Projected Activities
- Summary



REGULAR MEETING

- Worldwide SSCS Chapter Chairs Meeting
 - Feb. 11, 2009 at San Francisco (with ISSCC)
- SSCS Tainan Chapter Annual Meeting
 - Aug. 5, 2009 at Hualien (with VLSI Design/CAD Symp.)
- Tainan Section Meeting I
 - Aug. 25, 2009 at Tainan
- Region 10 SSCS Chapter Chairs Meeting
 - Nov. 17, 2009 at Taipei (with A-SSCC)
- Tainan Section Meeting II 
 - Dec. 4, 2009 at Tainan



Staff List

- Advisory Board – Prof. Chua-Chin Wang (NSYSU)
- Chair – Prof. Soon-Jyh Chang (NCKU)
- Secretary – Prof. Shuenn-Yuh Lee (CCU)
- Treasurer – Prof. Tzuen-Hsi Huang (NCKU)
- Educational Affairs – Prof. Tsung-Heng Tsai (CCU)
- Professional Affairs – Prof. Chien-Hung Tsai (NCKU)
- Membership Affairs – Prof. Hung-Yu Wang (KUAS)



WEBSITE

- <http://sscas.ee.ncku.edu.tw/ieeee/sscs/>



ACTIVITIES & ACCOMPLISHMENTS

- Technical Speech
 - Speaker : Prof. Yeo Kiat Seng (NTU, Singapore)
 - Topic : CMOS RF Technology and integrated Circuit Design: Can It Keep Pace With Moore's Law?
 - Participators : 47



ACTIVITIES & ACCOMPLISHMENTS

- Co-sponsor 2009 IC Design Contest



2009 IC Design Contest 九十七學年度大專校院 積體電路設計競賽

競賽日期 初賽：2009年4月1日（於各學校實驗室）
決賽：2009年5月6日-8日（新竹CIC）

研究類組 比賽類別

<input type="radio"/> 研究所組	<input type="radio"/> 大學組
A. 標準元件數位電路設計	D. 標準元件數位電路設計
B. 全客戶設計	E. 全客戶設計
<input type="radio"/> 不分組	F. 可程式邏輯設計
C. 類比電路設計	

報名期間 2009年2月9日（一）上午9點開始
2009年3月2日（一）下午5點截止

競賽網址 <http://campusweb.yuntech.edu.tw/icdesign/index.html>

報名方式

1. 報名隊伍由指導老師一位、學生兩位組成（可跨系跨校）。
2. 指導老師所推薦參賽隊伍數不限，但每人每隊僅能報名一組。
3. 採線上報名，填寫書面報名資料，將以下資料備齊：
(1) 報名表 (2) 指導教授同意暨參賽切結書

以掛號郵寄至承辦單位：640 雲林縣斗六市大學路3段123號
國立雲林科技大學 電子工程系 IC 競賽小組 收

相關單位

<input type="radio"/> 承辦人：許明華教授、陳育謙教授	<input type="radio"/> 傳真：05-5312063
<input type="radio"/> 聯絡人：何嘉芳小姐	<input type="radio"/> 電話：05-5342601 轉 4306

指導單位：YUTP 主辦單位：SOC 承辦單位：YUTP 協辦單位：CIC IEEE CASS SSCS Tainan Chapter Tainan Chapter

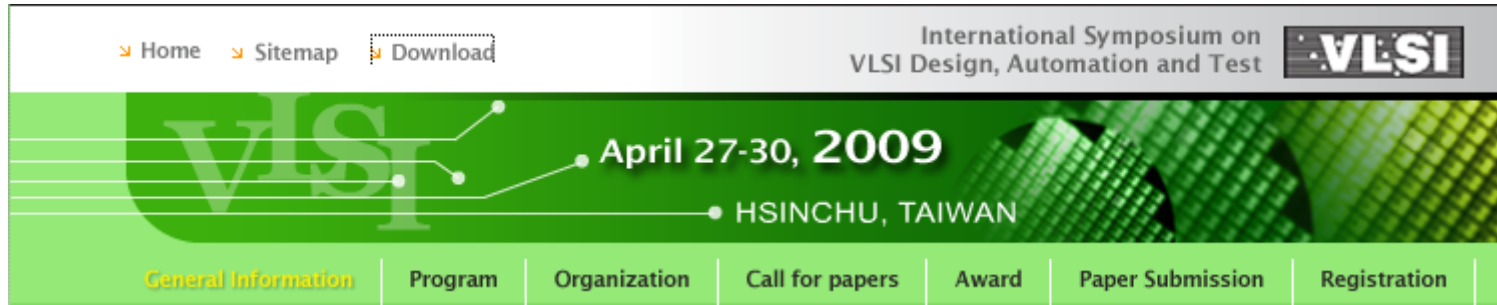
贊助單位：Altera Cadence Mentor Graphics SpringSoft SUN Synopsys Xilinx

協助單位：金屬科技、凌陽科技、創意電子、智原科技、瑞昱半導體、鼎威研發



ACTIVITIES & ACCOMPLISHMENTS

- Cosponsor 2009 VLSI-DAT



Home Sitemap Download

International Symposium on VLSI Design, Automation and Test **VLSI**

April 27-30, 2009
HSINCHU, TAIWAN

General Information Program Organization Call for papers Award Paper Submission Registration

More VLSI-DAT information will keep in updating. For details about VLSI-TSA, please visit the website at <http://vlsitsa.itri.org.tw>

- Sponsored by

- Industrial Technology Research Institute, Taiwan
- Dept. of Industrial Technology, MOEA, Taiwan
- Ministry of Education, Taiwan

- In cooperation with

- Chinese Institute of Electrical Engineers
- IEEE Circuits and Systems Society
- IEEE Circuits and Systems Society Taipei/Tainan Chapter
- IEEE Computer Society Taipei/Tainan Chapter
- IEEE Electron Devices Society
- IEEE Electron Devices Society Taipei/Tainan Chapter
- IEEE Solid-State Circuits Society
- IEEE Solid-State Circuits Society Taipei/Tainan Chapter
- IEEE Taipei/Tainan Section
- Taiwan IC Design Society
- Taiwan Semiconductor Industry Association

- Industrial Sponsors



ALI 揚智科技
ALI Corporation

ATOTECH

Etron 鈺創科技

GLOBAL UNICHIP CORP. 創意電子

漢民

HOLTEK 錸昌半導體股份有限公司
HOLTEK SEMICONDUCTOR CORP.

MXIC

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NAN YA

REALTEK
Realtek Semiconductor Corp.

SpringSoft

tsmc 台積電

winbond

Date :
VLSI-TSA April 27-29, 2009
VLSI-DAT April 27-30, 2009



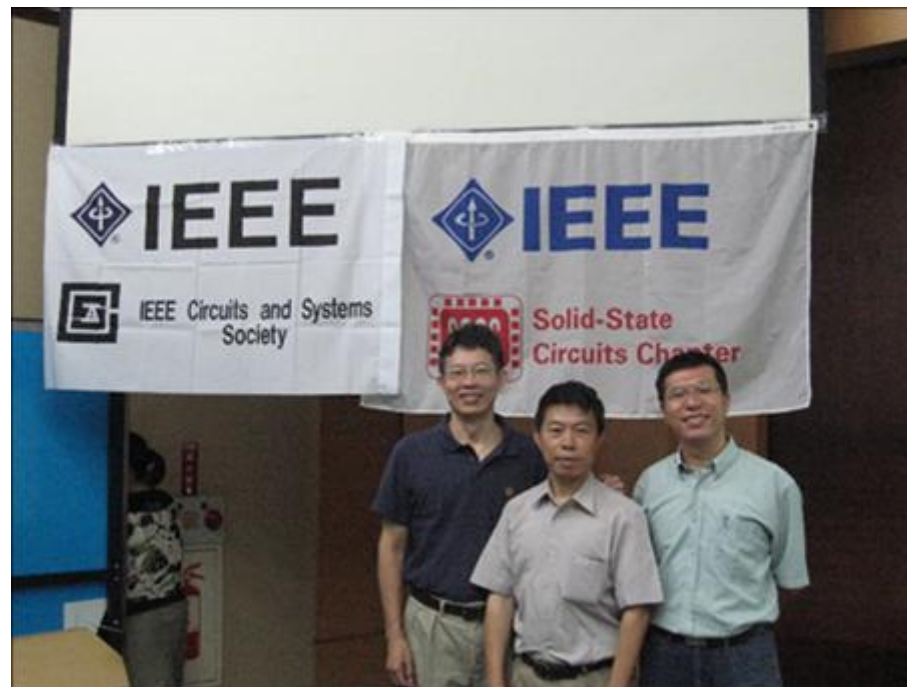
ACTIVITIES & ACCOMPLISHMENTS

- SSCS/CASS joint annual meeting with Taipei Chapter at Hualien



ACTIVITIES & ACCOMPLISHMENTS

- Technical short course
 - Speaker : Prof. J.-J. Tang (STC/ITRI)
 - Topic : Introduction to 3D IC Design/Process
 - Participators : 112



ACTIVITIES & ACCOMPLISHMENTS

- Technical short course
 - Speaker : Prof. Jung-Chih Chiao (EE, UTA)
 - Topic : Biochip Roadmap: Implantable Electronics
 - Participators : 35



ACTIVITIES & ACCOMPLISHMENTS

- Cosponsor 2009 A-SSCC



A-SSCC

IEEE Asian Solid-State Circuits Conference

Second Call for Papers



IEEE Asian Solid-State Circuits Conference (A-SSCC) 2009

Location: The Grand Hotel, Taipei, Taiwan

Date: November 16, 2009 (Monday) – November 18, 2009 (Wednesday)

Sponsored by IEEE SSCS, IEEE Region-10 SSCS Chapters

The IEEE A-SSCC 2009 (Asian Solid-State Circuits Conference) is an international forum for presenting the most updated and advanced chips and circuit designs in solid-state and semiconductor fields. The conference is supported by the IEEE Solid-State Circuits Society and will be held in Asia. Further details on the conference and paper submission guidelines and templates will be available at the A-SSCC official website <http://a-sscc.org/> around the beginning of April, 2009.



IEEE SSCS Tainan Chapter

Projected ACTIVITIES

- Member Development
 - (Dec. 9) Talk at National Kaohsiung University of Applied Sciences
- Cosponsor 2010 VLSI Design/CAD Symposium
- Cosponsor 2010 ISNE (International Symposium on Next-Generation Electronics)



Summary

- Accomplishments (Jan. 2009 – Nov. 2009)
 1. Staff recruitment
 2. Website set up - <http://sscas.ee.ncku.edu.tw/ieee/sscs/>
 3. Three technical speeches
 - CMOS RF Technology and integrated Circuit Design: Can It Keep Pace With Moore's Law?
 - Introduction to 3D IC Design/Process
 - Biochip Roadmap: Implantable Electronics
 4. Co-sponsor IC Design Contest, VLSI-DAT and A-SSCC
- Projected Activities (Dec 2009 - Dec. 2010)
 1. Member Development
 2. Cosponsor two symposiums

