

IEEE SSC Society
Tainan Chapter
Annual Report

Presented by: Soon-Jyh, Chang



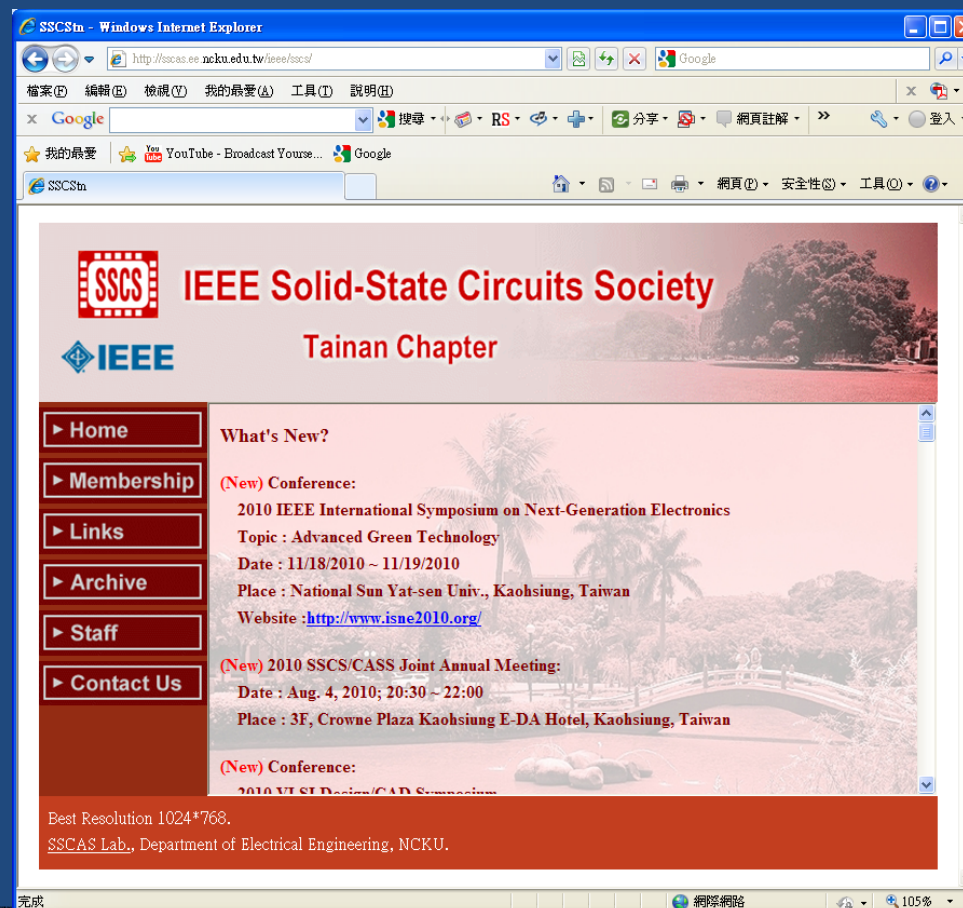
2010
Annual Meeting
Tainan Section

Chapter Officers

- Members: 43 (07) → 48 (08) → 53 (09) → 59 (10)
- Staff List
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 - Membership Affairs – Prof. Hung-Yu Wang (KUAS)

WEBSITE

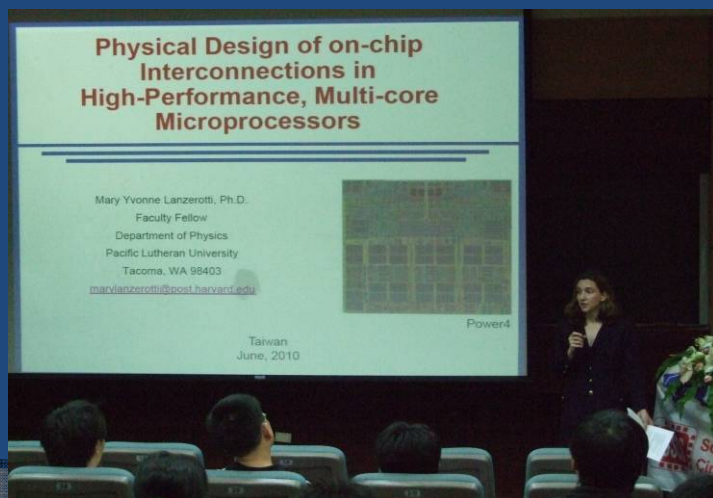
◎ <http://sscas.ee.ncku.edu.tw/ieee/sscs/>



Chapter Activities

Technical Speech

- Topic: The Physical Design of On-Chip Interconnections in High-Performance Microprocessors
- Speaker: Dr. Mary Yvonne Lanzerotti (Editor-in-Chief of the SSC Magazine)
- Place: ATI room, Dept. of EE, NCKU, Tainan, Taiwan



Chapter Activities

Technical Speech

- Topic: Design Optimization on Analog Circuits
- Speaker: Dr. Li-Da Huang (Magma Design Automation, Inc.)
- Place: ATI room, Dept. of EE, NCKU, Tainan, Taiwan



Chapter Activities

Technical Speech

- Topic: RF IC Design in Deep Submicron CMOS
- Speaker: Prof. John R. Long (Delft Univ., Netherlands)
- Place: ATI room, Dept. of EE, NCKU, Tainan, Taiwan
- Attendance: 26



Chapter Activities

● Technical Speech

- Topic: AC Design and Performance of LDOs
- Speaker: Prof. Gabriel Rincón-Mora (George Tech., USA)
- Place: Room 92119, NCKU/EE, Tainan, Taiwan



Chapter Activities

- Co-sponsor
2010 IC Design
Contest

九十八學年度大學校院 積體電路設計競賽

競賽日期 初賽：2010年3月24日（於各學校實驗室）
決賽：2010年4月28日～30日（新竹CIC）

研究所組	大學組	比賽類別
A. 標準元件數位電路設計	D. 標準元件數位電路設計	
B. 全客戶設計	E. 全客戶設計	
◎不分組	F. 可程式邏輯設計	
C. 類比電路設計		

報名期間 2010年2月1日（一）上午9點開始
2010年2月28日（日）下午5點截止

競賽網址 <http://campusweb.yuntech.edu.tw/~icdesign/2010/index.html>

報名方式

- 報名隊伍由指導老師一位、學生兩位組成（可跨系跨校）。
- 指導老師所推薦參賽隊伍數不限，但每人每隊僅能報名一組。
- 採線上報名，填寫書面報名資料，將以下資料備齊：
(1) 報名表 (2) 指導教授同意暨參賽切結書
以掛號郵寄至承辦單位：640 雲林縣斗六市大學路3段123號
國立雲林科技大學 電子工程系 IC競賽小組 收

相關單位

- ◎ 承辦人：許明華教授、陳育鑽教授 ◎ 傳真：05-5312063
- ◎ 聯絡人：何嘉芳小姐 ◎ 電話：05-5342601 轉 4306

指導單位：CIC 主辦單位：SOC 承辦單位：CIC 承辦單位：CIC

贊助單位：Altera、Cadence、Mentor Graphics
SpringSoft、SUN、Synopsys、Xilinx
協助單位：台灣類比科技、凌陽科技、智原科技
瑞昱半導體、義隆電子、鼎威科技、
聯傑國際

【依筆劃字母順序排列】

2010
IC
Design Contest

Chapter Activities

Co-sponsor 2010 VLSI Design/CAD Symposium



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陳顯升、陳廣仁、麥偉基、黃世安
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謝志成、謝紹德、謝獻榮、羅翠玉
蘇志偉



第二十一屆超大型積體電路設計暨計算機輔助設計技術研討會

The 21st VLSI Design / CAD Symposium

August 3-6, 2010

義大皇冠假日飯店



Call for Papers

因應 SoC 設計複雜度不斷的提升以及 IC 產品應用的多樣化，本屆大會將以「Towards Multiple Dimensions in SoC Design」為主題，誠摯邀請產、官、研、學各界先進，就「後SoC」時代的未來發展，進行一系列之專題演講、座談、短期課程訓練、及論文發表。

論文範圍：可應用於（但不限於）計算機、通訊、多媒體、消費性電子、生醫、與能源之下列研究主題

Regular Sessions A : Digital IC and SoC Design	Regular Sessions B : EDA
<ol style="list-style-type: none"> 1. Signal Processing ICs 2. Communication ICs 3. ASIC, ASIP 4. System Architecture Design (including SW/HW) 5. Single-Core and Multi-Core 6. Reconfigurable Architectures 7. FPGA Rapid Prototyping 	<ol style="list-style-type: none"> 1. System-Level Modeling and Simulation/Verification 2. Synthesis and Optimization 3. Timing, Power, Thermal Analysis and Optimization 4. Signal/Power Integrity, Interconnect Modeling and Simulation 5. Physical Design Automation 6. Design for Manufacturability/Yield 7. Testing and Debug

Regular Sessions C : Analog/Mixed-Signal/RF Design	Special Sessions
<ol style="list-style-type: none"> 1. Data Converters 2. RF/MM-Wave Circuits 3. Wireless Circuits and Transceivers 4. Wireline Circuits and Transceivers 5. PLLs, DLLs, and Clock Generators 6. I/O, Interfaces, Sensors, Imagers, Displays, and MEMS 7. Analog/Power-Management Circuits and Systems 	<ol style="list-style-type: none"> a : 3-Dimensional Visual Computing and ICs b : Green Power ICs and Systems c : Biomedical ICs and Health-Care Technology

◎ 本年度大會新增 3 個 special sessions，相關資訊請參閱大會網頁。

論文格式：原以英文書寫，請用A4，Double-Column 10點格式，最多四頁。

論文書寫請參照IEEE標準格式，參考範例請見大會活動網頁，格式不符恕不接受投稿！

投稿方式：一律電子投稿，限用PDF檔案格式，投稿若獲接受，均以原稿刊登，恕不再接受修改。

重要日期：投稿截止日期：2010.05.24

論文接受通知：2010.06.29

註冊截止日期：2010.07.09

聯絡方式：周怡婷、許雅菁助理 Tel : 06-2761834 Fax : 06-2761749

E-mail : cad2010@visilab.ee.ncku.edu.tw

大會網址：<http://vlsicad2010.ee.ncku.edu.tw/>

主辦單位：台灣積體電路設計學會 <http://www.ticd.org.tw/>

承辦單位：成大晶片系統研發中心、成大電機系、成大資工系

協辦單位：教育訓練研究室SOC總聯盟、國科會工程處、晶片系統國家型科技計畫辦公室、

國家晶片系統設計中心(CIC)、IEEE Taipei/Tainan Sections、

IEEE SSCS Taipei/Tainan Chapters、IEEE CAS Society Taipei/Tainan Chapters

Chapter Activities

Co-sponsor 2010 International Symposium on Next- Generation Electronics

International Symposium on Next-Generation Electronics (ISNE-2010)

November 18-19, 2010

National Sun Yat-sen Univ., Kaohsiung, Taiwan

<http://www.isne2010.org>



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Waisum Wong, *SMIC, China*
Cary Yang, *Santa Clara Univ., USA*
Paul Zhou, *Analog Devices, USA*

Call for Participation

Advanced Green Technology

The 1st International Symposium on Next-Generation Electronics (ISNE) will be held in National Sun Yat-sen University, Kaohsiung, TAIWAN on Nov. 18-19, 2010. The theme of the ISNE-2010 is "Advanced Green Technology". The symposium seeks to provide a platform for technical exchanges and communications shared by experts from all over the world. The purpose is to bring together scientists and engineers actively engaged in research and development on solid-state manufacturing technologies, devices, circuits, and systems to discuss current progress in next-generation electronics. Original unpublished papers are solicited. Papers accepted for presentation at the symposium will also be considered for inclusion in a special issue of The Inter. J. of Electrical Engineering (IJEE) and J. of the Chinese Institute of Engineers (JCIE). More ISNE-2010 information will keep in updating. For details about ISNE, please visit the website at <http://www.isne2010.org>. Prospective authors are invited to submit a camera-ready paper of maximum four pages in length, including figures and references. Original papers are solicited on the topics including, but not limited to:

Paper Registration

Non-IEEE and Non-TIEEE member :

	Regular	Early bird	Normal	On site
Registration	9/10-10/10	10/11-11/11	11/12-11/18	
	350 USD	400 USD	450 USD	

IEEE or TIEEE member :

	Early bird	Normal	On site
Registration	9/10-10/10	10/11-11/11	11/12-11/18
	300 USD	350 USD	400 USD

Non-IEEE and Non-TIEEE student member :

	Student	Early bird	Normal	On site
Registration	9/10-10/10	10/11-11/11	11/12-11/18	
	150 USD	200 USD	450 USD	

IEEE or TIEEE student member :

	Early bird	Normal	On site
Registration	9/10-10/10	10/11-11/11	11/12-11/18
	100 USD	150 USD	200 USD

Program

Time	Nov. 18 (Thur.)	Time	Nov. 19 (Fri.)
08:00-08:20	Registration & Check In	08:30-08:40	Registration & Check In
08:20-08:40	Opening Remark	08:40-08:50	Award Ceremony
08:40-09:40	Keynote Talk 1: Prof. Xing Zhou	08:50-10:00	Keynote Talk 2: Prof. Myung Hoon
09:40-10:00	Coffee Break	10:00-10:20	Coffee Break
10:00-11:00	Invited Talk 1 - Prof. Jin He	10:20-11:10	Invited Talk 3 - Prof. Mohamad Sawan
11:10-12:00	Invited Talk 2 - Dr. Yoshiji Horikoshi	11:10-12:00	Invited Talk 4 - Prof. Ming-Dou Ker
12:00-13:30	Luncheon	12:00-13:30	Luncheon
13:30-15:00	Technical Session 1 & Poster Session 1	13:30-15:00	Technical Session 3 & Poster Session 2
15:00-15:30	Coffee Break	15:00-15:30	Coffee Break
15:30-17:00	Technical Session 2 & Poster Session 1	15:30-17:00	Technical Session 4 & Poster Session 2
17:00-18:00	NSYSU Campus Tour		

Supported by



E-mail : pmlee@mail.stut.edu.tw (Prof.Lee) / giri-hsf@vlsi.ee.nsysu.edu.tw (Ms.Huang) Tel : +886-7-5252000 ext.4149 Fax : +886-7-5254199

2010

Annual Meeting



Tainan Section

Aug. 4, 2010

Chapter Activities

- SSCS/CASS joint annual meeting with Taipei Chapter at Kaohsiung, Taiwan



Others (Promotions)

Member Development

- Topic: (1) Energy-efficient ADCs Design & (2) Introduction to IEEE SSCS
- Speaker: Prof. Soon-Jyh Chang
- Place: NCNU/EE, Nantou, Taiwan
- Attendance: ~ 80



Others (Promotions)

- IEEE SSC Magazine Interview
 - Reporter: Dr. Mary Yvonne Lanzerotti
 - Editor-in-Chief of the SSC Magazine
 - Jun. 29, 2010



Conclusion

- ◎ SSCS Tainan is a young and small chapter, promotion activities to increase the number of members are particularly needed
- ◎ Thanks